

AEG 7/13/09

Amendments to the Specification

Please amend the title as follows:

Monolithic Power Semiconductor Structures Including Pairs of Integrated Devices

AEG 7/13/09

Please re-write paragraph [0029] as indicated below:

[0029] AEG 7/13/09

[0029] FIG. 9 depicts a second~~seventh~~ aspect of the present invention in accordance with the teachings presented herein.

Please re-write the Abstract as indicated below:

Provided herein are exemplary embodiments of mMonolithic semiconductor structures having at least two pairs of two lateral e~~on~~constructed semiconductor devices combined on a first surface of a single semiconductor substrate. Embodiments include connected source terminals defining common source terminals.